

SHEET 1 OF 1

FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE  INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)	ATTY. DOCKET NO. 2933SE-11-CIP-2	SERIAL NO. 09/037,674
	APPLICANT MIZUHARA et al.	
	FILING DATE March 9, 1998	GROUP ART 2811

## U.S. PATENT DOCUMENTS

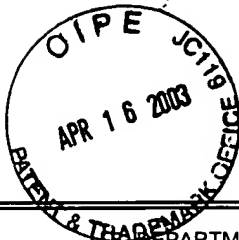
*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROP.
ON	AA	5,459,086	10/17/95	Yang	437	35	

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION	
							YES	NO

## OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)


EXAMINER  ON	DATE CONSIDERED  6/6/03
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

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*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROP.
ON	AA	5,192,697	03/09/93	Leong	437	37	
ON	AB	5,818,068	10/06/98	Sasaki et al.	257	59	

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION	
							YES	NO

## OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

ON	AC	Wolf, S.; Silicon Processing for the VLSI Era; Vol. 2, Lattice Press, pp. 232-233.

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